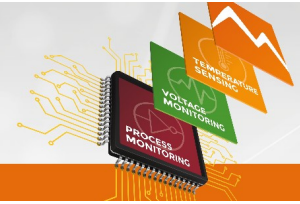


# Role: Digital Design Engineer

Reporting to: Design Centre Manager

Based: Gdansk, Poland

In-Chip Monitoring  
Subsystem Solutions



## About the Company:

Moortec Semiconductor is no ordinary company. We are looking for talented and enthusiastic self-starters with inquiring minds who are willing to support our cutting edge work in semiconductor IP design. We licence our in-chip monitoring products to a range of Blue Chip companies around the globe. From modest beginnings in 2005, we have now established ourselves as market-leaders and a forward-thinking global company and an industry employer of choice.

Our working environment is welcoming and friendly and you'll be surrounded by some of the brightest minds in the industry and world-class Engineers. For some of our roles it doesn't matter if you're not from an engineering background but it's always important you can share our company values of; **Expertise, Teamwork, Integrity and Balance.**

We can offer employees a challenging and fulfilling career path, where you will be encouraged to let your ideas flourish at every level and be supported in your development to fulfil your potential.

We offer an excellent salary and benefits package, flexible working arrangements, regular social events and modern offices.

## About The Role:

Working as part of a small but expanding team, you will be an experienced Digital Design Engineer with responsibility for the design, verification and testing of the digital and mixed signal solutions for which Moortec Semiconductor has an industry leading reputation.

## Qualifications, Skills & Experience:

We are seeking a digital design engineer with at least 3 years of solid experience with the following "essential" and "preferred" skills and qualifications.

### Essential Skills:

- Bachelors, Masters, PhD or equivalent qualification in Electronic Engineering or a related field
- A wide and varied understanding of digital design techniques and practices.
- Experience of functional verification and related topics—code coverage, functional coverage for example.
- Ability to develop RTL in SystemVerilog and or Verilog.
- A sound understanding of ASIC digital physical implementation flow, (synthesis, place and route, STA)

### Preferred:

Development experience of digital and mixed signal IP blocks.

Practical hands on experience in the testing and validation of integrated circuits.

Programming and scripting skills in C, python, tcl, bash.

### Responsibilities and Duties:

- Draft and detail, digital block design specifications.
- Design and simulate circuits to agreed requirement specifications.
- Create supporting design documents such as design reports and user documentation.
- Liaise with other technical staff to facilitate best possible engineering solutions.
- Provide support in answering customer questions and attending some customer meetings
- Other duties as may be required.

### Salary & Benefits:

- Competitive salary
- Supportive career development
- Company bonus scheme
- Standard holiday + public holidays
- Life insurance cover
- Private Healthcare package
- Access to company share scheme