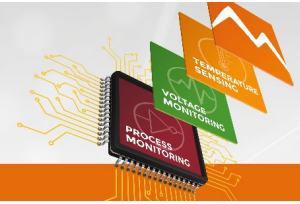


Role: Principle Digital Design Engineer

Reporting to: Design Centre Manager

Based: Gdansk, Poland

In-Chip Monitoring
Subsystem Solutions



About the Company:

Moortec Semiconductor is no ordinary company. We are looking for talented and enthusiastic self-starters with inquiring minds who are willing to support our cutting edge work in semiconductor IP design. We licence our in-chip monitoring products to a range of Blue Chip companies around the globe. From modest beginnings in 2005, we have now established ourselves as market-leaders and a forward-thinking company with a truly global reach.

Our working environment is welcoming and friendly and you'll be surrounded by some of the brightest minds in the industry and world-class Engineers. For some of our roles it doesn't matter if you're not from an engineering background but it's always important you can share our company values of; **Expertise, Teamwork, Integrity and Balance.**

We can offer employees a challenging and fulfilling career path, where you will be encouraged to let your ideas flourish at every level and be supported in your development to fulfil your potential.

We offer an excellent salary and benefits package, flexible working arrangements, regular social events and modern offices.

About The Role:

- Provide technical leadership for engineering design projects, specify, architect, design, simulate, and verify IC circuits, blocks, or full chips. Mentor and develop team members supporting the succession needs of the organisation.

Responsibilities and Duties:

- Provide technical lead on a range of engineering projects from a digital perspective.
- Draft and ratify block requirement specifications.
- Lead peer review process and act as a driver for quality assurance within engineering.
- Assist engineering management with project planning and feedback on technical status of projects.
- Communicate directly with customer where technical input is required.
- Design and simulate circuits to agreed requirement specifications.
- Create appropriate documentation to support designs (For example; Design Report)
- Liaise with other technical staff to facilitate best possible engineering solutions.
- Provide mentoring of junior engineering staff as required.
- Other duties as may be required.

Qualifications, Skills & Experience:

- Bachelors, Masters, PhD or equivalent qualification in Electronic Engineering or a related field
- Previous technical lead experience in a similar role
- A solid background working on Digital Design for ASICs
- A sound working knowledge of ASIC digital physical implementation flow, from design specification to RTL coding, synthesis and verification
- Strong RTL coding ,ideally SystemVerilog
- A good understanding of current functional and formal verification techniques.
- Great communication skills, a real team focus, with a passion for collaborative team work

Preferred:

- An appreciation of hardware and software requirements in SoC design.
- An awareness of mixed signal IP development.
- Programming and scripting skills in C, Python, tcl, bash (Desirable).

Salary & Benefits:

- Competitive salary
- Supportive career development
- Company bonus scheme
- Standard holiday + public holidays
- Life insurance cover
- Private Healthcare package
- Access to company share scheme