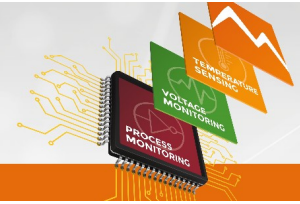


Role: Principle Digital Design Engineer

Reporting to: Engineer Manager

Based: Plymouth, UK

In-Chip Monitoring
Subsystem Solutions



About the Company:

Moortec Semiconductor is no ordinary company. We are looking for talented and enthusiastic self-starters with inquiring minds who are willing to support our cutting edge work in semiconductor IP design. We licence our in-chip monitoring products to a range of Blue Chip companies around the globe. From modest beginnings in 2005, we have now established ourselves as market-leaders and a forward-thinking global company and an industry employer of choice.

Our working environment is welcoming and friendly and you'll be surrounded by some of the brightest minds in the industry and world-class Engineers. For some of our roles it doesn't matter if you're not from an engineering background but it's always important you can share our company values of; **Expertise, Teamwork, Integrity and Balance.**

We can offer employees a challenging and fulfilling career path, where you will be encouraged to let your ideas flourish at every level and be supported in your development to fulfil your potential.

We offer an excellent salary and benefits package, flexible working arrangements, regular social events and modern offices in one of the most beautiful areas of the UK. We're close to outstanding beaches and exceptional coastline, rolling hills and lush green countryside, the dramatic moors and all the entertainment, shops and restaurants offered by Plymouth; Britain's Ocean City.

About The Role:

Working as part of an expanding team, you will be an experienced Principle Digital Design Engineer with responsibility for the project management and leadership of various design, verification and testing of digital and mixed signal solutions for which Moortec Semiconductor has an industry leading reputation.

Qualifications, Skills & Experience:

- A minimum of 10 years experience of providing people and technical leadership for engineering design projects, specify, architect, design, simulate, and verify IC circuits, blocks, or full chips.

Essential

- Bachelors, Masters, PhD or equivalent qualification in Electronic Engineering or a related field .
- Previous technical lead experience in a similar role.
- A background working on Digital Design for ASICs .
- A good working knowledge of ASIC digital physical implementation flow, from design specification to RTL coding, synthesis and verification.
- Strong RTL coding ,ideally Verilog-based.
- Great communication skills, a real team focus, with a passion for collaborative team work.
- A great sense of humour.

Desirable

- Programming and scripting skills in C, Python, tcl, bash (Desirable)

Responsibilities and Duties:

- Provide technical lead on a range of engineering projects from a digital perspective.
- Draft and ratify block requirement specifications.
- Lead peer review process and act as a driver for quality assurance within engineering.
- Assist engineering management with project planning and feedback on technical status of projects.
- Communicate directly with customer where technical input is required.
- Design and simulate circuits to agreed requirement specifications.
- Create appropriate documentation to support designs (For example; Design Report)
- Liaise with other technical staff to facilitate best possible engineering solutions.
- Provide mentoring of junior engineering staff as required.
- Other duties as may be required.

Salary & Benefits:

- Competitive salary
- Supportive career development
- Regular pay reviews and company bonus scheme
- 28 days holiday + bank holidays
- Company pension scheme
- Life assurance cover
- HealthShield cash plan
- Gym Membership
- Access to company share scheme