

Role: Principal Analog IC Design Engineer

Reporting to: Design Centre Manager

Based: Gdansk Poland

In-Chip Monitoring
Subsystem Solutions



About the Company:

Moortec Semiconductor is no ordinary company. We are looking for talented and enthusiastic self-starters with inquiring minds who are willing to support our cutting edge work in semiconductor IP design. We licence our in-chip monitoring products to a range of Blue Chip companies around the globe. From modest beginnings in 2005, we have now established ourselves as market-leaders and a forward-thinking global company and an industry employer of choice.

Our working environment is welcoming and friendly and you'll be surrounded by some of the brightest minds in the industry and world-class Engineers. For some of our roles it doesn't matter if you're not from an engineering background but it's always important you can share our company values of; **Expertise, Teamwork, Integrity and Balance.**

We can offer employees a challenging and fulfilling career path, where you will be encouraged to let your ideas flourish at every level and be supported in your development to fulfil your potential.

We offer an excellent salary and benefits package, flexible working arrangements, regular social events and modern offices.

About The Role:

As part of the expanding engineering team, this role will involve technical leadership within engineering, as well as hands on design, simulation and verification of full custom analog and mixed-signal circuits and IP blocks, on some of the most advanced semiconductor nodes available.

Qualifications, Skills & Experience:

Essential Skills:

- 10+ years of experience in analog/mixed-signal IC design.
- Proven track record of delivering tasks on schedule.
- Degree (or equivalent qualification) in electronic engineering.
- Good communication and interpersonal skills.
- Good knowledge of Cadence tools.

Qualifications, Skills & Experience:

Desired:

- Knowledge of Cadence tools is highly desirable.
- Familiarity with mixed–signal simulation flows

Continuous Professional Development:

With the support and direction of the company, we actively encourage and expect all team members to continually develop the technical skills, competencies and experience in order to meet the current and future needs of the business.

Responsibilities and Duties:

- Provide technical leadership for members of the analog design team.
- Promote design best practices and drive improvements in quality.
- Oversee peer reviewing process.
- Hands on design, simulation and verification of analog block designs.
- Support the CTO with planning, and management of engineering projects and roadmap development.
- Provide technical support to junior engineers.
- Provide expert support, where appropriate, to customers.

Salary & Benefits:

- Competitive salary
- Supportive career development
- Company bonus scheme
- Standard holiday + public holidays
- Life insurance cover
- Private Healthcare package
- Access to company share scheme