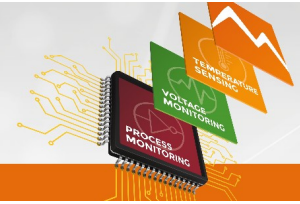


Role: Lab Engineer

Reporting to: Engineer Manager

Based: Plymouth, UK

In-Chip Monitoring
Subsystem Solutions



About the Company:

Moortec Semiconductor is no ordinary company. We are looking for talented and enthusiastic self-starters with inquiring minds who are willing to support our cutting edge work in semiconductor IP design. We licence our in-chip monitoring products to a range of Blue Chip companies around the globe. From modest beginnings in 2005, we have now established ourselves as market-leaders and a forward-thinking global company and an industry employer of choice.

Our working environment is welcoming and friendly and you'll be surrounded by some of the brightest minds in the industry and world-class Engineers. For some of our roles it doesn't matter if you're not from an engineering background but it's always important you can share our company values of; **Expertise, Teamwork, Integrity and Balance.**

We can offer employees a challenging and fulfilling career path, where you will be encouraged to let your ideas flourish at every level and be supported in your development to fulfil your potential.

We offer an excellent salary and benefits package, flexible working arrangements, regular social events and modern offices in one of the most beautiful areas of the UK. We're close to outstanding beaches and exceptional coastline, rolling hills and lush green countryside, the dramatic moors and all the entertainment, shops and restaurants offered by Plymouth; Britain's Ocean City.

About The Role:

Working as part of a small but expanding team, you will be an experienced device test and validation engineer with responsibility for the development and implementation of hardware and software for the testing and validation of Moortec digital and mixed signal IP.

Qualifications, Skills & Experience:

Essential Skills:

- For this role you'll need to have a University degree or similar, either in Electronics Engineering or related field.
- A sound understanding of programming and operating the National Instruments LabVIEW environment.
- FPGA design using HDL, i.e. VHDL and/or SystemVerilog.
- Design of digital and analogue circuits for device validation and testing.
- PCB schematic capture using OrCad or similar.
- Good understanding of device test methods.

Desired:

- Programming and scripting skills in VB script, python, tcl & bash.
- An understanding of statistical and data analysis.
- Experience of using the National Instruments DIAdem database analysis tool
- Understanding of silicon test data

Continuous Professional Development:

With the support and direction of the company, we actively encourage and expect all team members to continually develop the technical skills, competencies and experience in order to meet the current and future needs of the business.

Responsibilities and Duties:

- Draft and detail test and validation specifications and plans.
- Design hardware test platforms for device validation
- Write test program software
- Develop test reports.
- Liaise with application support and technical staff to provide assistance with customer support questions
- Other duties as required

Salary & Benefits:

- Competitive salary
- Supportive career development
- Company bonus scheme
- 28 days holiday + bank holidays
- Company pension scheme
- Life assurance cover
- HealthShield cash plan
- Gym Membership
- Access to company share scheme