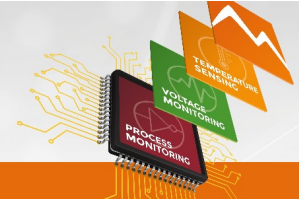


Role: Principal Analog IC Design Engineer

Reporting to: CTO

Based: Plymouth, UK

In-Chip Monitoring
Subsystem Solutions



About the Company:

Moortec Semiconductor is a rapidly expanding semiconductor IP design company. We licence products to a range of Blue Chip companies around the globe, to the new and emerging field of in-chip monitoring.

About The Role:

As part of the expanding engineering team, this role will involve technical leadership within engineering, as well as hands on design, simulation and verification of full custom analog circuits and IP blocks, on some of the most advanced semiconductor nodes available.

Responsibilities and Duties:

- Provide technical leadership for members of the analog design team.
- Promote design best practices and drive improvements in quality.
- Oversee peer reviewing process.
- Hands on design, simulation and verification of analog block designs.
- Support the CTO with planning, and management of engineering projects and roadmap development.
- Provide technical support to junior engineers.
- Provide expert support, where appropriate, to customers.

Experience

- Significant experience in analog/mixed-signal IC design.
- Proven track record of delivering tasks on schedule.
- Degree (or equivalent qualification) in electronic engineering.
- Good communication and interpersonal skills.
- Good knowledge of Cadence tools.

Salary & Benefits:

- Competitive salary and benefits package.