



Silicon Design Services

Moortec Semiconductor provides the complete set of skills required to design and verify analog and mixed-signal silicon. Our significant track record in this field allows us to actively help you to minimize the risks and expense of semiconductor design.

We strive to create a close working relationship with our clients, to help you to maximize the capability and performance of your silicon. Our service portfolio includes:

- Analogue Block Design
- Custom Chip Design
- Platform/Product design



You choose ...



Transistor level analog/RF IC design (BiCMOS, CMOS 0.6 μ m to 65nm)
Full custom layout
IP - PLLs, oscillators/VCOs, bandgaps, LNAs, I/O, ESD, power-on resets, etc



Analog & Digital block design
Digital implementation
SoC Verification
Chip level design methodologies and floor planning
Chip test, packaging and delivery



Hardware, firmware and embedded software design
Modules for IC testing, load boards, evaluation boards and reference designs
Platform and Product design

Moortec - the perfect partner to provide high performance analog / mixed signal IP and support for your SoC and Digital ASIC designs

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