



Description

MR74034 – Configurable Outputs
0.18um CMOS Multi-Standard Differential and Multi-Voltage LVCMOS Output Drivers

Part Number:

MR74034

Features:

- High performance
- Low Phase Jitter
- PECL
- HCSL
- LVDS
- LVCMOS at 3.3v, 2.5v, 1.8v & 1.5v
- HSTL
- SSTL_3
- SSTL_2

Technology:

TSMC 0.18um 1.8V,
3.3V Standard CMOS

Deliverables:

GDSII
Abstract
Design Report
Verilog-A Models

Circuit Status:

Silicon Proven

Design rights:

Customer owned.

Overview:

Multi-Standard Differential/Single-Ended Output Driver comprising tristatable drivers wire-OR'ed onto two output pads.

Differential Output frequencies to 350MHz
PECL
HCSL (part of PCI Express)
LVDS (TIA/EIA-644-A point-to-point)
Rise/Fall time < 512ps
Output Duty Cycle PECL 48-52% HCSL 45%-55%
Output Jitter <1ps rms (wide band noise)
Supply Voltage 3.3v (+/-10%)
Power Consumption:
PECL 70mW
HCSL 70mW
LVDS 50mW

Power Down
Junction Temp -40C to 125C

LVCMOS Output frequencies to 160MHz
Supply Voltages Supported 3.3v, 2.5v, 1.8v & 1.5v
Also supports HSTL, SSTL_3 & SSTL_2 standards
Programmable Drive Strength
Programmable Slew Rate
Output Duty Cycle 48%-52% & 45%-55% (freq/load dependent)
Output Jitter <1ps rms (wide band noise)
Power Down
Junction Temp -40C to 125C

Symbol View:

